

Sub B'
A1 cancelled

diode is connected to the pad and the negative [ring] line.

4. (Amended) The chip of claim 1 wherein the ESD positive [ring] line encircles the periphery of the chip.

A2

5. (Amended) The chip of claim 1 wherein the ESD negative [ring] line encircles the periphery of the chip.

6. (Amended) The chip of claim 1 and further including an ESD switch connected to the ESD positive [ring] line and the ESD negative [ring] line.

7. (Amended) The chip of claim 1 and further including a plurality of ESD switches which are each connected to the ESD positive [ring] line and the ESD negative [ring] line.

Please add the following new claim:

A3

--31. The chip of claim 1 and further comprising:
a power line spaced apart from the EDS positive line; and
a transistor connected between the power line and the pad.--

REMARKS

This is a combined preliminary amendment to the above-identified application, and a response to an election of species requirement. With respect to the preliminary amendment, claims 1 and 4-7 have been